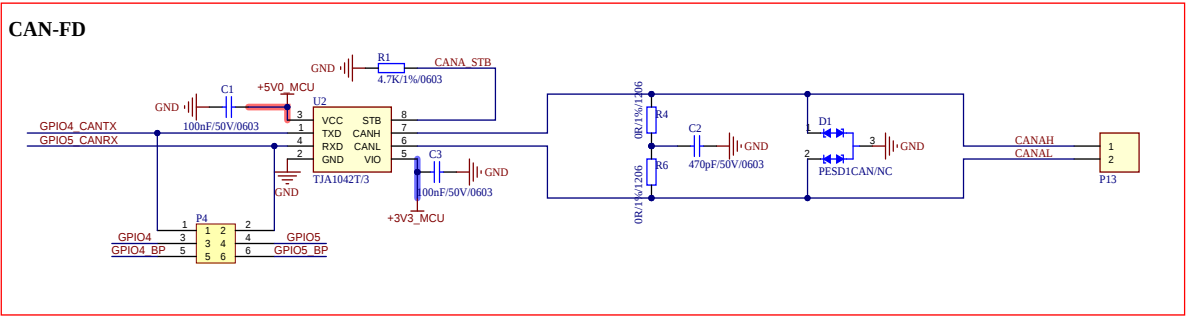
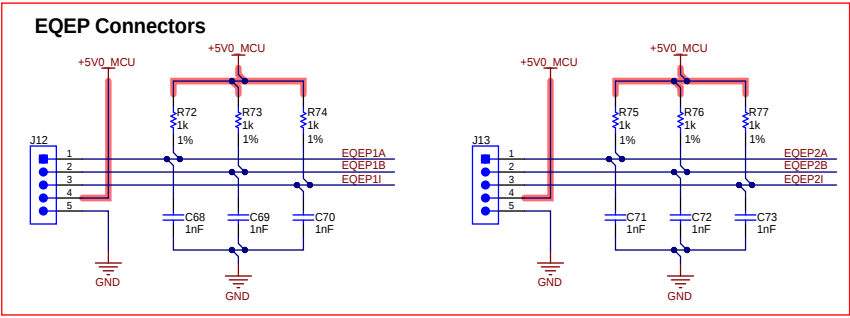
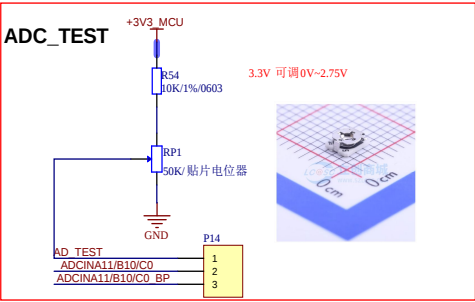
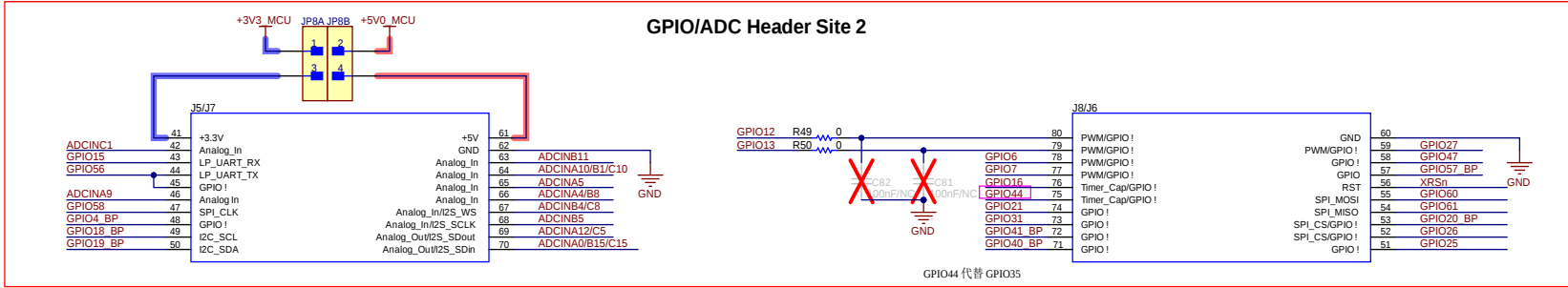
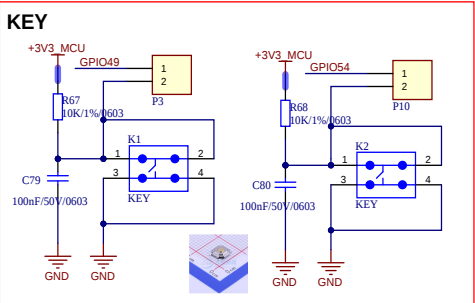
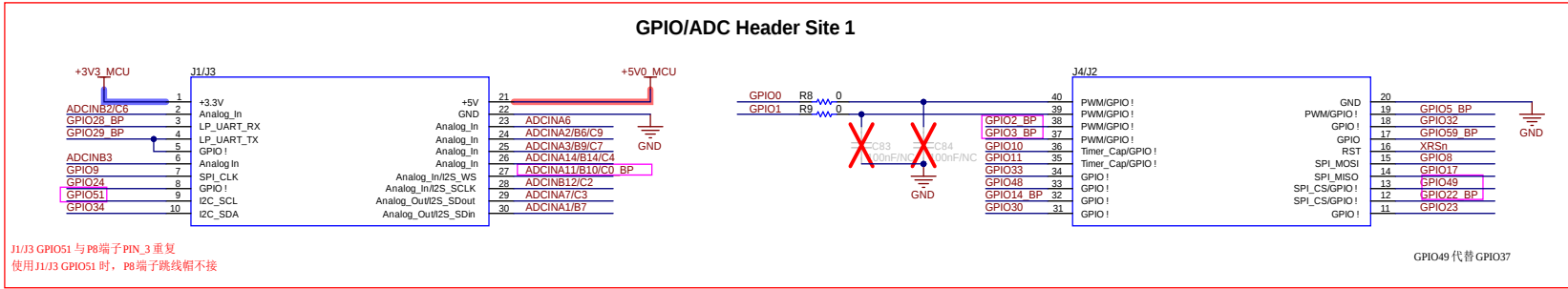
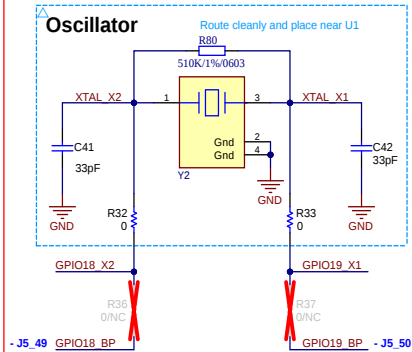


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Size	Number	Revision
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Date:	6/06/2025	Sheet of
File:	D:\Desktop\Xplorer-GS32F0039_Connectors\DrawBy:	



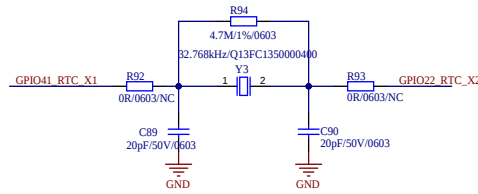
By default:

- Crystal Y2 is connected between GPIO18_X2 and GPIO19_X1.
- GPIO18_BP AND GPIO19_BP are connected to the GPIO/ADC headers.

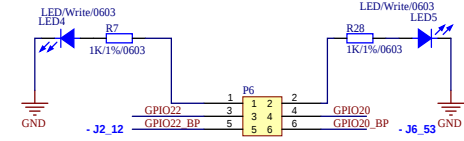
If GPIO18 and GPIO19 are needed at the GPIO/ADC Headers:

- Remove R32 and R33, populate R36 and R37 with 0 ohm resistors
- The G3280039 device's internal oscillator will need to be used

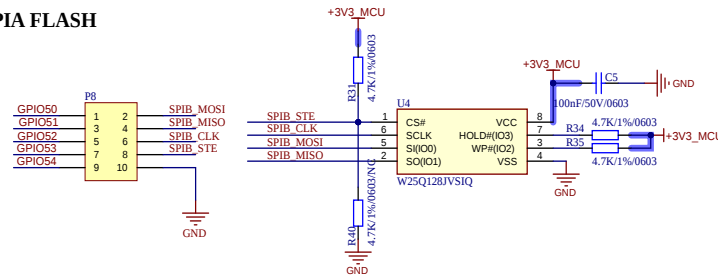
RTC



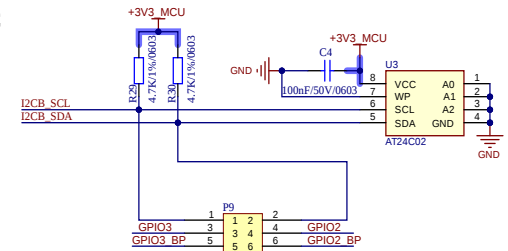
LED



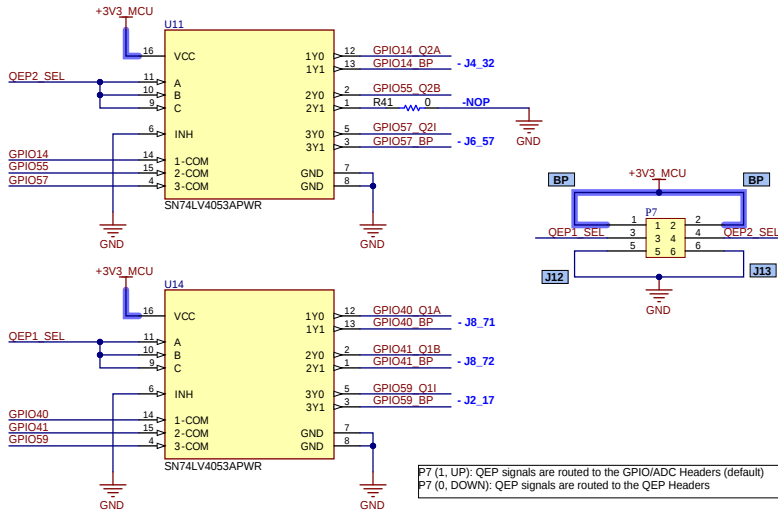
SPIA FLASH



I2C

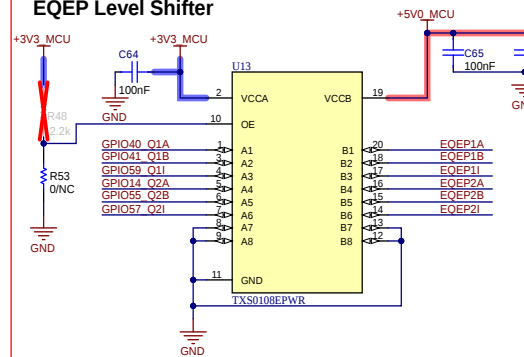


EQEP 选通



P7 (1, UP): QEP signals are routed to the GPIO/ADC Headers (default)
P7 (0, DOWN): QEP signals are routed to the QEP Headers

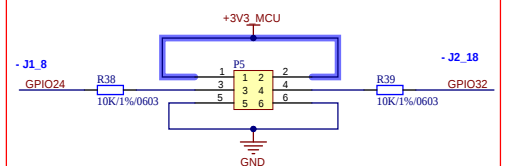
EQEP Level Shifter



To disable the Level Shifter:

1. De-populate R48
2. Place a 0Rohm resistor on R53

Boot Mode Select



Selected Boot Mode Chart

Mode #	GPIO24	GPIO32	Boot Mode
00	0	0	Boot from Parallel GPIO
01	0	1	Boot from SCI / Wait Mode
02	1	0	Boot from CAN
03	1	1	Boot from Flash

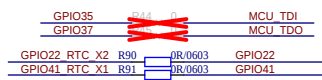
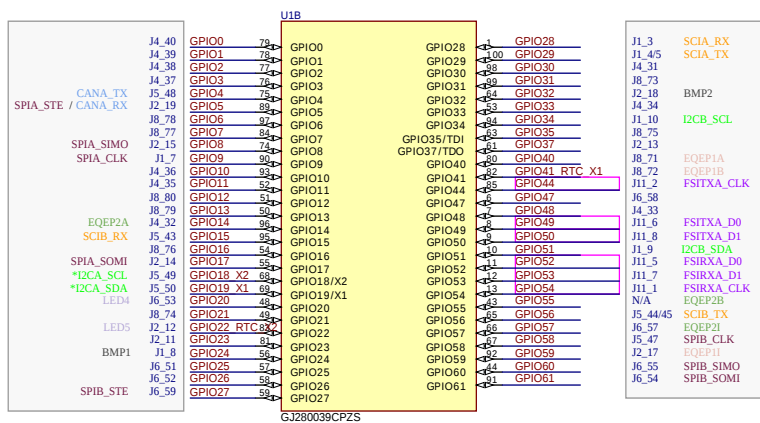
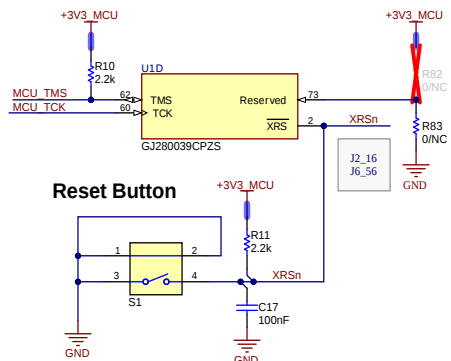
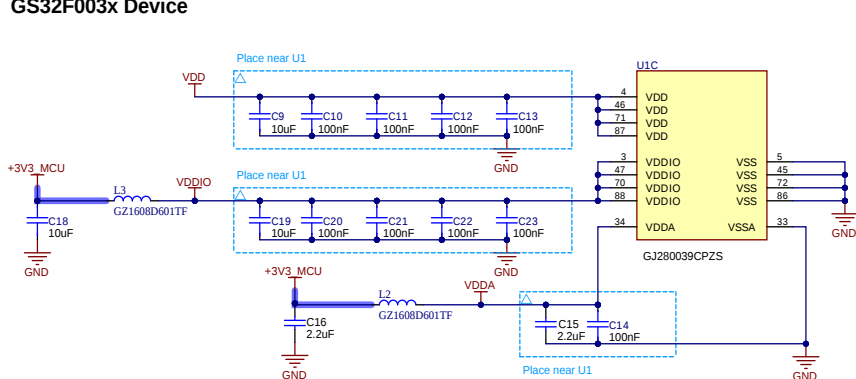
BOOT 模式表待定

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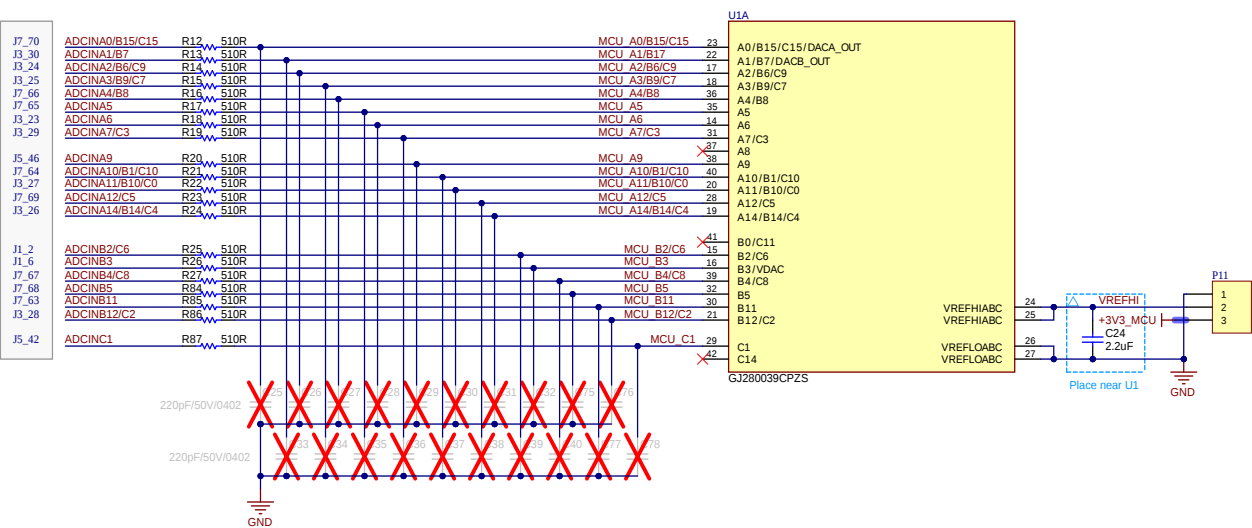
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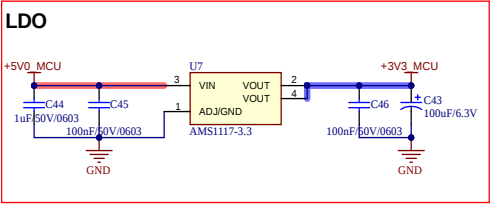
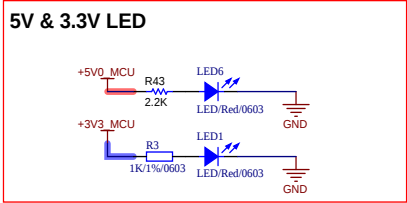
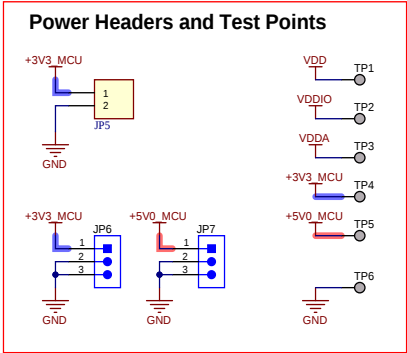
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Size	Number		Revision
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Date:	6/06/2025		Sheet of
File:	D:\Desktop\Xplorer-GS32F0039_Peripheral		5000 By:

GS32F003x Device



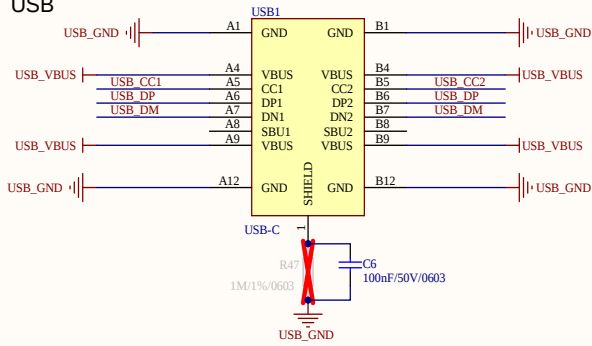
默认使用 GPIO41(EOEP) 和GPIO22 (LED)，电阻焊接



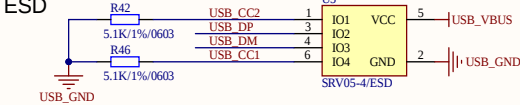


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		6	

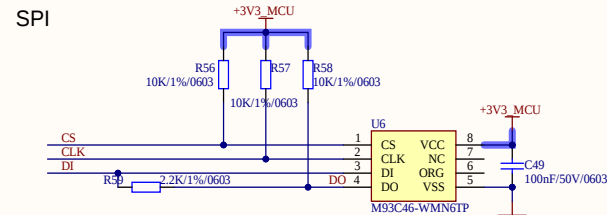
USB



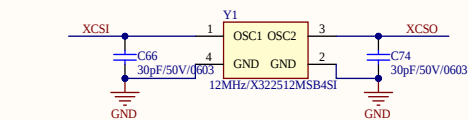
ESD



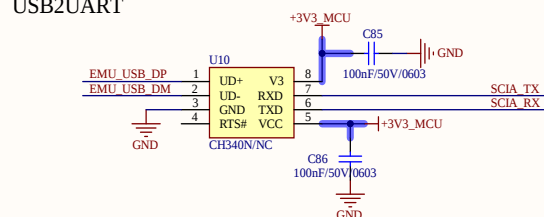
SPI



Oscillator

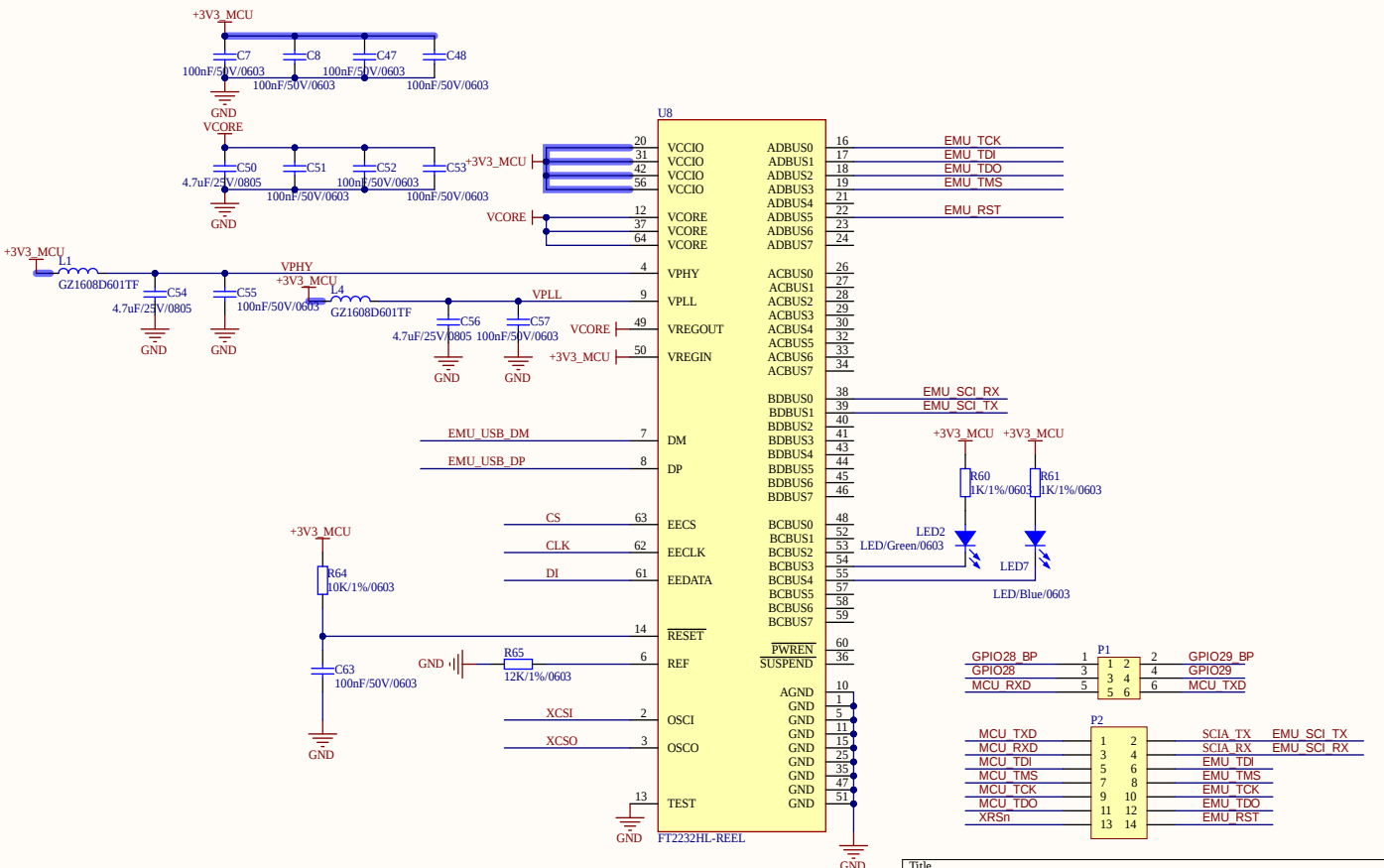
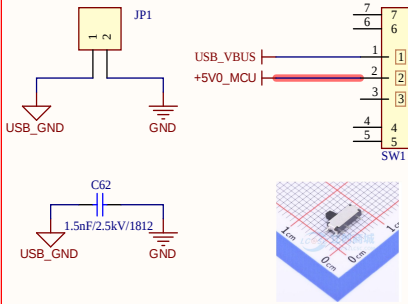


USB2UART



与板载调试器使用同一组串口，U10与U8 BOM 选择

Isolation Boundary



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Title			Xplorer-GS32F0039_FT2232		
Size	Number	Revision			
A3	4				
Date:	6/06/2025	Sheet of			
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